

Electronic Acknowledgement Receipt

EFS ID:	4087436
Application Number:	09987978
International Application Number:	
Confirmation Number:	7905
Title of Invention:	LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE TWO
First Named Inventor/Applicant Name:	Yujiro Kajihara
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement Letter	32049RV1IDS.pdf	59966 9e1e676629313ab94ee184826b6715fc1ec1441	no	2
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed (SB/08)	32049RV1SB08.pdf	130221 e59b0645aabbd232cfe182ccda57258d208992e24	no	1
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	Information Disclosure Statement Letter	32049OA.pdf	333994 2f1849138e6f570c20e8a4959c727568dd091116	no	5
Warnings:					
Information:					
4	Foreign Reference	32049REF.pdf	268195 743c5461f471aa3bf12cf53250ecd273d96e2a8	no	8
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	31008 ee31810361191bbd69ace78f42bb30f50b0cb5d4	no	2
Warnings:					
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Total Files Size (in bytes):			823384		
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